

# Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

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Application ID:

Title of Invention:

10666324

**DUAL STAGE VOLTAGE** 

**REGULATION CIRCUIT** 

First Named Inventor:

Nicola Telecco

Domestic/Foreign Application:

**Domestic Application** 

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Information Disclosure

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Attorney Docket Number:

ATM-228

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#### **TRANSMITTAL**

Electronic Version v1.1 Stylesheet Version v1.1.0

Title of Invention

**DUAL STAGE VOLTAGE REGULATION CIRCUIT** 

Application Number:

10/666324

Date:

2003-09-17

First Named Applicant:

Nicola

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Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney

Documents being submitted

**Files** 

us-ids

ATM228ids-usidst.xml

us-ids.dtd us-ids.xsl

Comments



## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Électronic Version v18 Stylesheet Version v18.0

> Title of Invention

**DUAL STAGE VOLTAGE REGULATION CIRCUIT** 

**Application Number:** 

10/666324

Confirmation Number:

7924

First Named Applicant: Nicola Telecco

Attorney Docket Number: ATM-228

Art Unit:

2838

Search string:

( 4835455 or 5831845 or 6114845 or 6188212 or

6194887 or 6362612 or 6369552 ).pn.

### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4835455	1989-05-30	Coddington et al.	A1	323	314
	2	5831845	1998-11-03	Zhou et al.	A1	363	60
	3	6114845	2000-09-05	Capici et al.	A1	323	313
	4	6188212	2001-02-13	Larson et al.	A1	323	281
	5	6194887	2001-02-27	Tsukada	A1	323	315
	6	6362612	2002-03-26	Harris	A1	323	312
	7	6369552	2002-04-09	Goyhenetche et al.	A1	323	266

### Signature

Examiner Name	Date		
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